

**IN THE CLAIMS:**

Please amend the claims as follows.

Claim 1 (Currently Amended): A semiconductor apparatus comprising a substrate, a semiconductor device formed on said substrate, and a protective film for sealing said semiconductor device, said semiconductor apparatus further comprising:

a first conductive layer in contact with a back surface of said protective film; and

a second conductive layer in contact with a front surface of said protective film;

a first electrode terminal electrically connected with the first conductive layer; and

a second electrode terminal electrically connected with the second conductive layer,

wherein each of the first electrode terminal and the second electrode terminal has a

surface area such that a metal probe can make contact therewith.

Claim 2 (Original): The semiconductor apparatus according to claim 1, further comprising an insulating film of electrical insulation formed on said semiconductor device, said first conductive layer being formed on said insulating film.

Claim 3 (Withdrawn): The semiconductor apparatus according to claim 1, wherein said semiconductor device includes an outermost electrode layer as said first conductive layer.

Claim 4 (Previously Presented): The semiconductor apparatus according to claim 1, wherein at least one of said first conductive layer and said second conductive layer is patterned into stripes.

Claim 5 (Previously Presented): The semiconductor apparatus according to claim 1, wherein said first conductive layer and said second conductive layer are patterned into stripes so as to cross each other.

Claim 6 (Canceled).

Claim 7 (Currently Amended): The semiconductor apparatus according to claim [[6]] 1, wherein said first and second electrode terminals are formed on a peripheral part of said substrate, said peripheral part being located outside an area in which said semiconductor device is formed.

Claim 8 (Currently Amended): The semiconductor apparatus according to claim [[6]] 1, wherein at least either one of said first electrode terminal and said second electrode terminal is made of a plurality of electrode pieces arranged at predetermined intervals along a peripheral part of said substrate.

Claim 9 (Previously Presented): The semiconductor apparatus according to claim 1, wherein said semiconductor device includes an electroluminescent device.

Claim 10 (Withdrawn): A method of manufacturing a semiconductor apparatus for detecting a defect within a protective film which seals a semiconductor device formed on a substrate, said method comprising the steps of:

(a) forming a first conductive layer;

(b) forming a protective film for covering said semiconductor device on said first conductive layer;

(c) forming a second conductive layer on said protective film; and

(d) measuring electrical conduction between said first conductive layer and said second conductive layer, and detecting a defect within said protective film based on the measurement result.

**Claim 11 (Withdrawn):** The method of manufacturing a semiconductor apparatus according to claim 10, further comprising the step of, after a defect of said protective film is detected in said step (d), forming a repair layer for covering a surface of said second conductive layer corresponding to at least a region of and in the vicinity of the detected defect of said protective film.

**Claim 12 (Withdrawn):** The method of manufacturing a semiconductor apparatus according to claim 10, further comprising the step of forming an insulating film of electrical insulation on said semiconductor device, and wherein said first conductive layer is formed on said insulating film in said step (a).

**Claim 13 (Withdrawn):** The method of manufacturing a semiconductor apparatus according to claim 10, wherein said semiconductor device includes an outermost electrode layer as said first conductive layer.

Claim 14 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 10, wherein said first conductive layer is patterned in stripes in said step (a).

Claim 15 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 10, wherein said second conductive layer is formed in stripes in said step (c).

Claim 16 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 10, wherein said steps (a) and (c) include patterning said first conductive layer and said second conductive layer in stripes so as to cross each other.

Claim 17 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 10, wherein: said step (a) includes forming a first electrode terminal in connection with said first conductive layer; and said step (c) includes forming a second electrode terminal in connection with said second conductive layer.

Claim 18 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 17, wherein said step (d) includes:

measuring electrical conduction between first and second probes, said first probe being in contact with a surface of either one of said first electrode terminal and said second electrode terminal, while scanning said second probe in contact with and across a surface of the other of said first electrode terminal and said second electrode terminal; and

identifying a region of the defect of said protective film based on the measurement result.

Claim 19 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 17, wherein said step (d) includes:

measuring electrical conduction between first and second probes, said first probe being into contact with a surface of either one of said first electrode terminal and said second electrode terminal, while sequentially connecting said second probe to a plurality of predetermined points on a surface of the other of said first electrode terminal and said second electrode terminal; and identifying a region of the defect: of said protective film based on the measurement results.

Claim 20 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 17, wherein, in each of said steps (a) and (c), said first and second electrode terminals are formed on a peripheral part of said substrate, said peripheral part being located outside an area in which said semiconductor device is formed.

Claim 21 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 17, wherein said step (a) includes forming a plurality of electrode pieces as said first electrode terminal, said electrode pieces being arranged at predetermined intervals along a peripheral part of said substrate.

Claim 22 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 17, wherein said step (c) includes forming a plurality of electrode pieces as said second electrode terminal, said electrode pieces being arranged at predetermined intervals along a peripheral part of said substrate.

Claim 23 (Withdrawn): The method of manufacturing a semiconductor apparatus according to claim 10, wherein said semiconductor device includes an electroluminescent device.